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APPLICANTS

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** CONTINUING DATA ^N ^{VS} *****

** FOREIGN APPLICATIONS ^N ^{VS} *****

IF REQUIRED, FOREIGN FILING LICENSE
 GRANTED

** 10/22/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>W. J. Peters</i> Examiner's Signature <i>VS</i> Initials	WA	4	20	2

ADDRESS

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TITLE

Method of mapping logic failures in an integrated circuit die

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RECEIVED 750		<input type="checkbox"/> 1.16 Fees (Filing)
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		<input type="checkbox"/> 1.18 Fees (Issue)
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